

**WEST****Freeform Search****Database:**

US Patents Full-Text Database  
 US Pre-Grant Publication Full-Text Database  
 JPO Abstracts Database  
 EPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

**Term:**
**Display:**  **Documents in Display Format:**  **Starting with Number** 
**Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Help

Logout

Interrupt

Main Menu

Show S Numbers

Edit S Numbers

Preferences

Cases

**Search History**
**DATE:** Monday, December 09, 2002   [Printable Copy](#)   [Create Case](#)
**Set Name Query**  
 side by side

**Hit Count Set Name**  
 result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L6</u>	L5 and (invalid\$6 near8 (parity or error))	1	<u>L6</u>
<u>L5</u>	L4 and (hit near6 (snoop\$4 or writ\$4))	42	<u>L5</u>
<u>L4</u>	L2 and (invalid\$6 near8 (shared or exclusive or own\$6 or assign\$4))	113	<u>L4</u>
<u>L3</u>	L2 and (invalid\$6 near8 (shared or exclusive or own\$4 or assign\$4))	112	<u>L3</u>
<u>L2</u>	L1 and ((bus or cache) near6 (snoop\$4 or watch\$4 or examin\$4 or monitor\$4))	186	<u>L2</u>
<u>L1</u>	invalid\$6 near8 ("L1" or lower or level) adj3 cache)	245	<u>L1</u>

END OF SEARCH HISTORY

WEST

## Freeform Search

Database:

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 IBM Technical Disclosure Bulletins

Term:

Display: 10 Documents in Display Format: TI Starting with Number 1

Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

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## Search History

DATE: Monday, December 09, 2002 [Printable Copy](#) [Create Case](#)Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L9</u>	L8 and (miss\$3 near8 (cache or level))	3	<u>L9</u>
<u>L8</u>	(cache or level) near6 invalidat\$3 near8 fault	8	<u>L8</u>
<u>L7</u>	(cache or level) near6 invalidat\$3 near8 fault	8	<u>L7</u>
<u>L6</u>	(cache or level) near6 invalidat\$3 near8 (error or parity)	45	<u>L6</u>
<u>L5</u>	cache near6 (invalidat\$3 or evict\$3) near8 (error or parity)	36	<u>L5</u>
<u>L4</u>	L3 and ((cache or bus) near6 (snoop\$4 or monitor\$3 or examin\$3 or watch\$3))	19	<u>L4</u>
<u>L3</u>	L2 and miss	28	<u>L3</u>
<u>L2</u>	L1 and ((invalidat\$3 or evict\$3) near8 (error or parity))	28	<u>L2</u>
<u>L1</u>	((711/117  711/122  711/124  711/141  711/144  711/146 )!.CCLS. )	1607	<u>L1</u>

END OF SEARCH HISTORY